

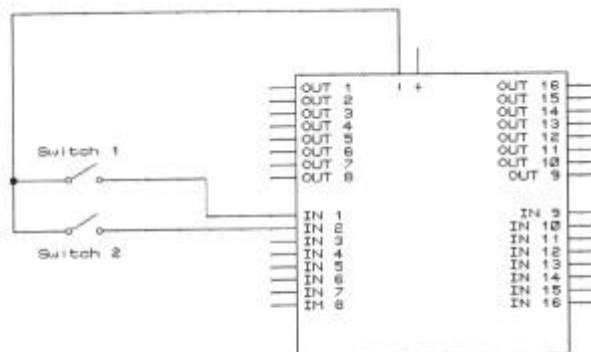
Logic demonstrator

Description

This application demonstrates the NOT, AND OR and Exclusive OR logic functions. Inputs 1 and two are the controlling inputs.

Ouput 1 is the inversion of input 1. Ouput 2 is the AND of the inputs. Output 3 is the OR of the inputs. Output 4 is the Exclusive OR of the inputs.

Wiring Diagram



Notes

the code makes use of virtual outputs 17 and 18 to generate the Exclusive OR function using a combination of AND, OR and NOT functions.

Improvements

Other functions could be added such as NAND and NOR.

Multiple input functions such as a 4 input function could be added.

Latching and reset functions could be added.